

27. (New) A lamination ceramic chip inductor, according to claim 26, wherein the plurality of conductive patterns include an electroformed conductive pattern having a shape of a straight line.

28. (New) A lamination ceramic chip inductor, according to claim 21, wherein the at least one pair of insulation layers are magnetic.

### **RESPONSE TO RESTRICTION REQUIREMENT**

In the Office Action, the Examiner issued a restriction requirement, in which the Examiner stated there are two distinct inventions, as follows:

- I. Claims 1-7 and 16-20, drawn to a method of forming a chip inductor, classified in class 29, subclass 602.1.
- II. Claims 8-15, drawn to a chip inductor, classified in class 336, subclass 200.

In accordance with the Examiner's request and with standard procedure, Applicants elect to prosecute claims 8-15, in Group II.

### **REMARKS**

Upon entry of the present amendment, claims 8-15 and 21-28 are pending in the present application. Claims 8, 11, 12 and 15 are amended, and non-elected claims 1-7 and 16-20 are canceled herein without prejudice. A total of 16 claims are now pending in the application. The newly submitted claims 21-28 include two independent claims. Canceled claims 1-7 and 16-20 include two independent claims. Therefore, no additional fee is believed due for the newly submitted claims.

Newly submitted claims 21-28 are supported in the claims and specification of the parent application as originally filed, and at page 26, lines 7-14 of the present specification. No new matter is added by these amendments.

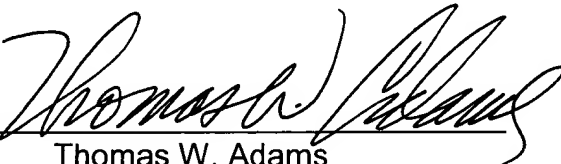
The amendments to claims 8, 11, 12 and 15 are made for clarification purposes only.

The Examiner and the Office are hereby informed that, of even date herewith, Applicants have filed a divisional application claiming priority to the present application and including the non-elected claims canceled herein.

Should the Examiner consider that a telephone interview would be helpful to facilitate favorable prosecution of the above-identified application, the Examiner is invited to contact the undersigned at the telephone number provided below. If any additional fees are required for the filing of these papers, Applicants request the Commissioner to charge those fees to deposit account #18-0988, docket no. YAMAP0347USB.

Respectfully submitted,  
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Date: January 15, 2001

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**APPENDIX**

The claims shown above in clean form have been amended as follows:

8. (Amended) A lamination ceramic chip inductor, comprising at least one conductive pattern, the [lamination ceramic chip inductor] at least one conductive pattern having a thickness of 10  $\mu\text{m}$  or more and a width to thickness ratio from 1 to less than 5.

11. (Amended) A lamination ceramic chip inductor, according to claim 8, further comprising at least one pair of insulation layers having the at least one conductive pattern formed therebetween.

wherein the at least one pair of insulation layers are magnetic.

12. (Amended) A lamination ceramic chip inductor, comprising at least one conductive pattern formed by an electroforming process using a photoresist, the [lamination ceramic chip inductor] at least one conductive pattern having a thickness of 10  $\mu\text{m}$  or more and a width to thickness ratio from 1 to less than 5.

15. (Amended) A lamination ceramic chip inductor, according to claim 12, further comprising at least one pair of insulation layers having the at least one conductive pattern formed therebetween.

wherein the at least one pair of insulation layers are magnetic.

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